Docket No.: 50090-465

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To be assigned

In re Application of

Masaki WATANABE

Serial No.: Divisional of

Appl. Serial No. 09/484,024

Filed: March 04, 2002

Examiner: To be assigned

PACKAGE FOR ACCOMMODATING ELECTRONIC PARTS, SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING PACKAGE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, DC 20231

Dear Sir:

For:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The references were cited by or submitted to the U.S. Patent and Trademark Office in parent application Serial No. 09/484,024, filed 01/18/2000, which is relied upon for an earlier Serial No.: Divisional of Appln. Serial No. 09/484,024

filing date under 35 USC 120. Thus, copies of these references are not attached. 37 CFR 1.98(d).

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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